

**WHAT IS CLAIMED IS:**

1           1. A wireless communication system comprising:  
2                 a radio module operable to communicate data between a host and at least  
3                 one external device;  
4                 at least one digital module operable to process data communicated by said  
5                 radio module;  
6                 a clock generator for generating first and second clock signals for use by  
7                 said digital module; and  
8                 power management logic operable to control said clock generator to cause  
9                 said clock generator to generate said first clock signal when said  
10                wireless communication system is operating in a first power mode  
11                and to generate said second clock signal when said wireless  
12                communication system is operating in a second power mode.  
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1           2.     The wireless communication system of claim 1 wherein said radio module  
2     is turned on when said wireless communication system is operating in said first power  
3     mode.

1           3.     The wireless communication system of claim 2, wherein said first clock  
2     signal is a high-speed clock generated by said clock generator when said radio module is  
3     turned on.

1           4.     The wireless communication system of claim 2, wherein said first clock  
2     signal is generated by a crystal and a phase-locked loop.

1           5.     The wireless communication system of claim 1, wherein said radio  
2     module is turned off when said communication system is operating in said second power  
3     mode.

1           6.       The wireless communication system of claim 5, wherein said second clock  
2 signal is a lower frequency clock that is generated by said clock generator when said  
3 radio is turned off.

1           7.       The wireless communication system of claim 5, wherein said second clock  
2 signal is generated by a low-power oscillator.

1           8.       The wireless communication system of claim 1, further comprising a timer  
2 operable to count clock cycles of said first and second clock signals.

1           9.       The wireless communication system of claim 8, further comprising a timer  
2 management module operable to maintain a cumulative count of the number of clock  
3 cycles counted by said timer during a predetermined time interval.

1           10.      The wireless communication system of claim 9, wherein said timer is  
2 operable to count the number of clock cycles for said first clock when said wireless  
3 communication system is operating in said first power mode and is further operable to  
4 count the number clock cycles for said second clock signal when said wireless  
5 communication system is operating in said second power mode.

1           11.      The wireless communication system of claim 10, wherein the number of  
2 clock cycles counted by said timer when said wireless communication system is  
3 operating in said second power mode is converted to an equivalent number of clock  
4 cycles that would have been generated by said first clock by using an adjustment factor  
5 based on the number of cycles said first clock would generate during a single cycle of  
6 said second clock.

1           12.      The wireless communication system of claim 9, wherein said timer is  
2 operable to count the number of clock cycles for said first clock when said wireless  
3 communication system is operating in said first power mode and said timer does not  
4 count the number of clock cycles for said second clock signal when said wireless  
5 communication system is operating in said second power mode.

1           13.     The wireless communication system of claim 12, wherein said timer  
2 management module is operable to generate updated timing information using  
3 information provided by said power management logic regarding the duration of the time  
4 interval that said wireless communication system is operating in said second power  
5 mode.

1           14. A method of managing power in a wireless communication system having a  
2 radio module operable to communicate data between a host and at least one external  
3 device and at least one digital module operable to process data communicated by said  
4 radio module, the method comprising:

5                 generating a high-frequency first clock signal for use by said digital  
6                         module when said wireless communication system is operating in a  
7                         first power mode and a lower frequency second clock signal for  
8                         use by said digital module when said wireless communication  
9                         system is operating in a second power mode; and

10                using power management logic to control said clock generator to cause  
11                         said clock generator to generate said first clock signal when said  
12                         wireless communication system is operating in a said first power  
13                         mode and to generate said second clock signal when said wireless  
14                         communication system is operating in said second power mode.

1           15.     The method of claim 14 wherein said radio module is turned on when said  
2 wireless communication system is operating in said first power mode.

1           16.     The method of claim 15, wherein said first clock signal is a high-speed  
2 clock that is generated by said clock generator when said radio module is turned on.

1           17.     The method of claim 15, wherein said first clock signal is generated by a  
2 crystal and a phase-locked loop.

1           18.     The method of claim 17, wherein said radio module is turned off when  
2 said communication system is operating in said second power mode.

1           19.     The method of claim 17, wherein said second clock signal is a lower  
2 frequency clock that is generated by said clock generator when said radio is turned off.

1           20.     The method of claim 19, wherein said second clock signal is generated by  
2     a low-power oscillator.

1           21.     The method of claim 14, further comprising using a timer to count clock  
2     cycles of said first and second clock signals.

1           22.     The method of claim 21, further comprising using a timer management  
2     module to maintain a cumulative count of the number of clock cycles counted by said  
3     timer during a predetermined time interval.

1           23.     The method of claim 22, further comprising using said timer to count the  
2     number of clock cycles for said first clock when said wireless communication system is  
3     operating in said first power mode and using said timer to count the number clock cycles  
4     for said second clock signal when said wireless communication system is operating in  
5     said second power mode.

1           24.     The method of claim 23, wherein the number of clock cycles counted by  
2     said timer when said wireless communication system is operating in said second power  
3     mode is converted to an equivalent number of clock cycles that would have been  
4     generated by said first clock by using an adjustment factor based on the number of cycles  
5     said first clock would generate during a single cycle of the said second clock.

1           25.     The method of 22, wherein said timer counts the number of clock cycles  
2     for said first clock when said wireless communication system is operating in said first  
3     power mode and said timer does not count the number of clock cycles for said second  
4     clock signal when said wireless communication system is operating in said second power  
5     mode.

1           26.     The method of claim 25, further comprising using said timer management  
2     module to generate updated timing information using information provided by said power  
3     management logic regarding the duration of the time interval that the wireless  
4     communication system is operating in said second power mode.